



**POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO
COLLEGE OF ENGINEERING & TECHNOLOGY**

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

N.SAIDA NAIK
S/O N.PEDA BALU NAIK
PALUVAYI POST
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GUNTUR DISTRICT
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Experience:

Teaching: 3Y 10 YEARS

Career Profile

Year	Organization	Role
2015-TILL DATE	Potti Sriramulu Chalavadi Mallikharjuna Rao college of Engg. And Tech., Vijayawada	Assistant Professor

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.TECH	Jntuk	2014	80
B.TECH	Anu	2012	75
INTERMEDIATE	Krishnaveni Jr. College	2008	79.5
SSC	Board Of Secondary Education	2006	78.3

The subjects handled:

1. ELECTRIC MACHINE II
2. ELECTRICAL CIRCUITS ANALYSIS II
3. POWER SEMICONDUCTOR DRIVES

4. CONTROL SYSTEMS
5. ELECTROMAGNETIC FORCE
6. Electrical Machine Design
7. Basic Electrical And Electronics Engineering

Affiliation to Professional Bodies

1. IAENG
2. THEIRED
3. ISTE

Publications

1. Power Quality Improvement in Microgrid” in International Journal of Power System Engineering, Science Journals 08(2014),PP:1-6, 1408-1(1)
2. Grid and Islanded Operation of a Distribution Generation Inverters in a Microgrid”in the journal of International Electrical Engineering Journal (IEEJ) Vol. 5 (2014) No.10, pp. 1553-1558 ISSN 2078-2365
3. Improved Performance of STATIC Compensator for Grid Connected Wind System Using IRP Theory” in the journal of IJCSM, Volume 1, Issue 1, September 2015, ISSN: 2455-1910
4. Control Technique of Microgrid With Kalman Filter” in the journal of IJCSM, Vol.3.Issue.1.2017 , ISSN: 2455-1910
5. Power Quality Improvement Using Unified Power Quality Conditioner (UPQC) Based on SVPWM Technique”in the journal of IJSETR, ISSN 2319-8885 Vol.06,Issue.13 April-2017, Pages:2530-2536
6. Contingency Analysis using Power System Analysis Toolbox” in the journal of International Journal of International Electrical Engineering Journal (IEEJ) Vol. 1 (2018) No.1 pp. 2488-2491 ISSN 2078-2365
7. Improvement of Power Quality By Using UPQC With SRF And UVT Control Techniques” in the journal of International journal of science and innovative engineering & technology , May 2017, issue volume 2, ISBN 978-81-904760-9-6

8. Current harmonics reduction using shunt active Filter in the journal of International Journal For Innovative Engineering and Management Research Vol 07 Issue12, Nov 2018 ISSN 2456 – 5083
9. Super Conducting Magnetic Energy Storage DVR based on Diode Clamped Modular Multilevel Converters in the journal of International Journal of Application or Innovation in Engineering & Management ISSN 2319 – 4847 Volume 7, Issue 11, November 2018
10. Improvement of Sag under Different Fault Conditions in the journal of International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-8 Issue-4, February 2019
11. Voltage Compensation Using PV-DVR with SEPIC Converter in the journal of International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-8 Issue-5 March, 2019
12. A Newly Designed Asymmetrical Multi-Cell Cascaded Multilevel Inverter for Distributed Renewable Energy Resources in the journal of International Journal of Recent Technology and Engineering (IJRTE) ISSN: 2277-3878, Volume-7, Issue-ICETESM, March 2019
13. Intelligent Hybrid Fuzzy Controller Based Zeta DC-DC Conversion Device for Brush-Less-DC Motor Drive Water Pumping System fed from PV in the journal of International Journal of Innovative Technology and Exploring Engineering (IJITEE) ISSN: 2278-3075, Volume-8 Issue-5 March, 2019
14. Published a book on “Grid and islanded operation of a dg inverters in microgrid” isbn-13: 978-6139894109, lap lambert academic publishing, 2018.
15. Harmonic source current reduction using shunt active filters with fuel cell in the journal of jasc: journal of applied science and computations issn no: 1076-5131 volume 6, issue 3, march /2019

Conferences, Seminars, Workshops & Guest Lectures Attended

1. Attended one day workshop on “recent trends in electrical engineering” on 7-December 2018 organized by VRSEC.

Events organized/ Conducted

1. Organized as a Coordinator for “INTERNET OF THINGS” in association with APSSDC on 26/11/2018 TO 01/12/2018.

N.SAIDA NAIK
Signature